

IN THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Cancelled)

2. (Original) A semiconductor integrated circuit device comprising a logic circuit having at least first and second blocks, first and second speed controllers, and a clock generator, characterized in that clock signals of different frequencies are supplied to the first and second blocks, and the first and second speed controllers control the operating speeds of the logic circuit in the blocks in accordance with the clock signals supplied to the respective blocks.

3. – 4. (Cancelled)